## In the Claims:

- 1-12 (canceled)
- 13. (currently amended) A membrane for use in conjunction with a semiconductor carrier which comprises:
  - (a) an electrically insulating substrate for application to a semiconductor carrier;
  - (b) an interconnect pattern on said substrate; and
- (c) a stud coupled to said interconnect pattern on over said substrate, said stud comprising a gold ball and a compliant material coating over a portion of said gold ball.
- 14. (previously presented) The membrane of claim 13 wherein said gold ball is the ball of a ball bond on said substrate.
  - 15. The membrane of claim 13 wherein said coating is a compliant epoxy resin.
  - 16. The membrane of claim 14 wherein said coating is a compliant epoxy resin.
- 17. The membrane of claim15 wherein said compliant material is filled with a material having sufficient hardness to be capable of penetrating the oxide film on the contact pads of semiconductor devices.

- 18. The membrane of claim15 wherein said compliant material is filled with a material having sufficient hardness to be capable of penetrating the oxide film on the contact pads of semiconductor devices.
  - 19. The membrane of claim 17 wherein said material is silver or silver-based flakes.
  - 20. The membrane of claim 18 wherein said material is silver or silver-based flakes.